

ABSTRACT

An input control apparatus capable of suppressing characteristic deterioration, reducing the circuit scale of a turbo decoder and effectively using memory of the turbo decoder. In this apparatus, control section (110) acquires information on a coding rate and coding block length of a received signal, determines the number of bits of systematic part Y1, and parity parts Y2 and Y3 in accordance with the coding rate and/or coding block length and so that the number of bits of one sequence of the parity parts falls below the number of bits of systematic part Y1 and controls bit number reduction section (109) so that the determined number of bits is obtained. Bit number reduction section (109) reduces the number of bits of systematic part Y1, and parity parts Y2 and Y3 output from separation section (108) under the control of control section (110) and decoder (111) performs turbo decoding using each sequence reduced by bit number reduction section (109).